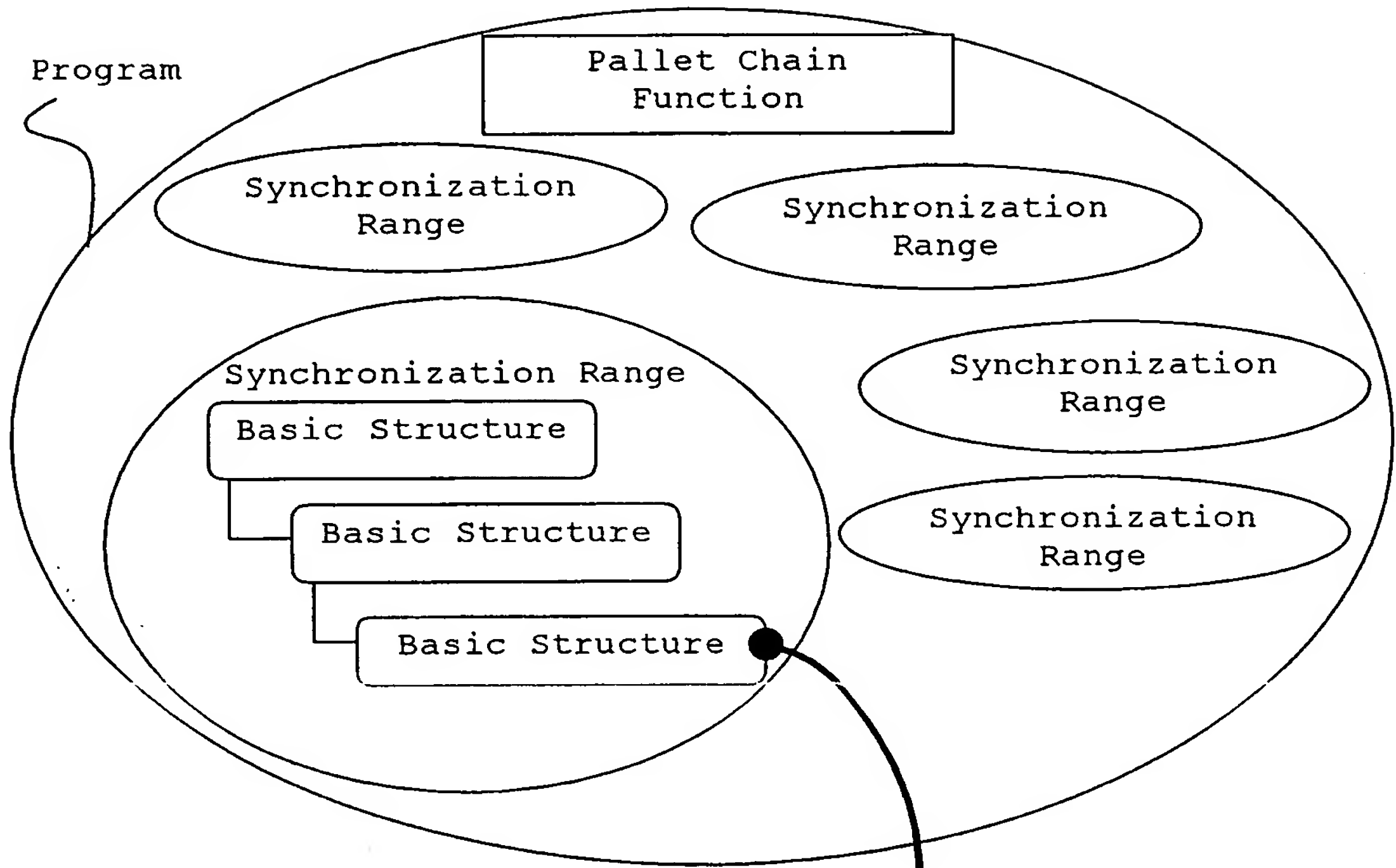
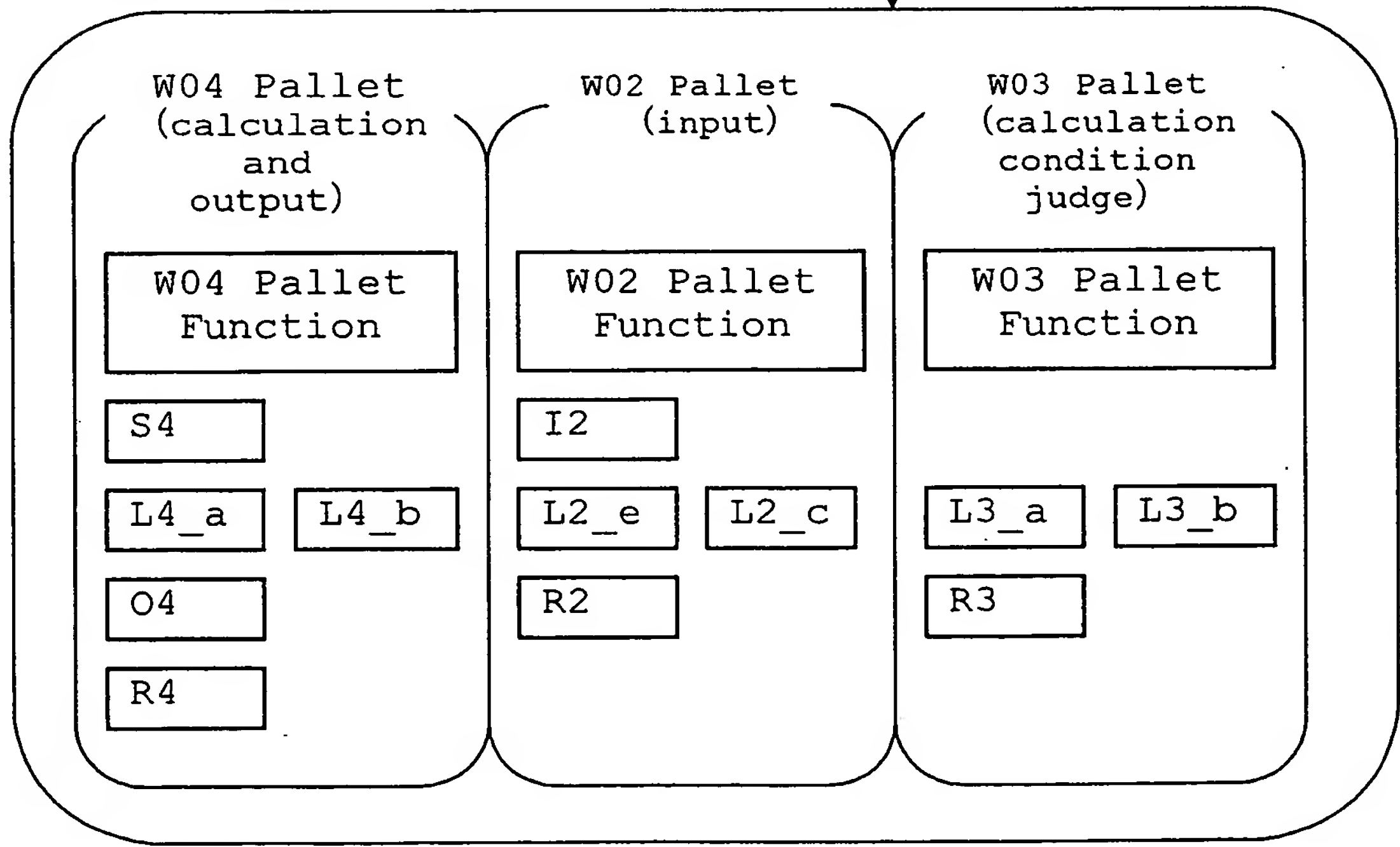


[Fig. 1]

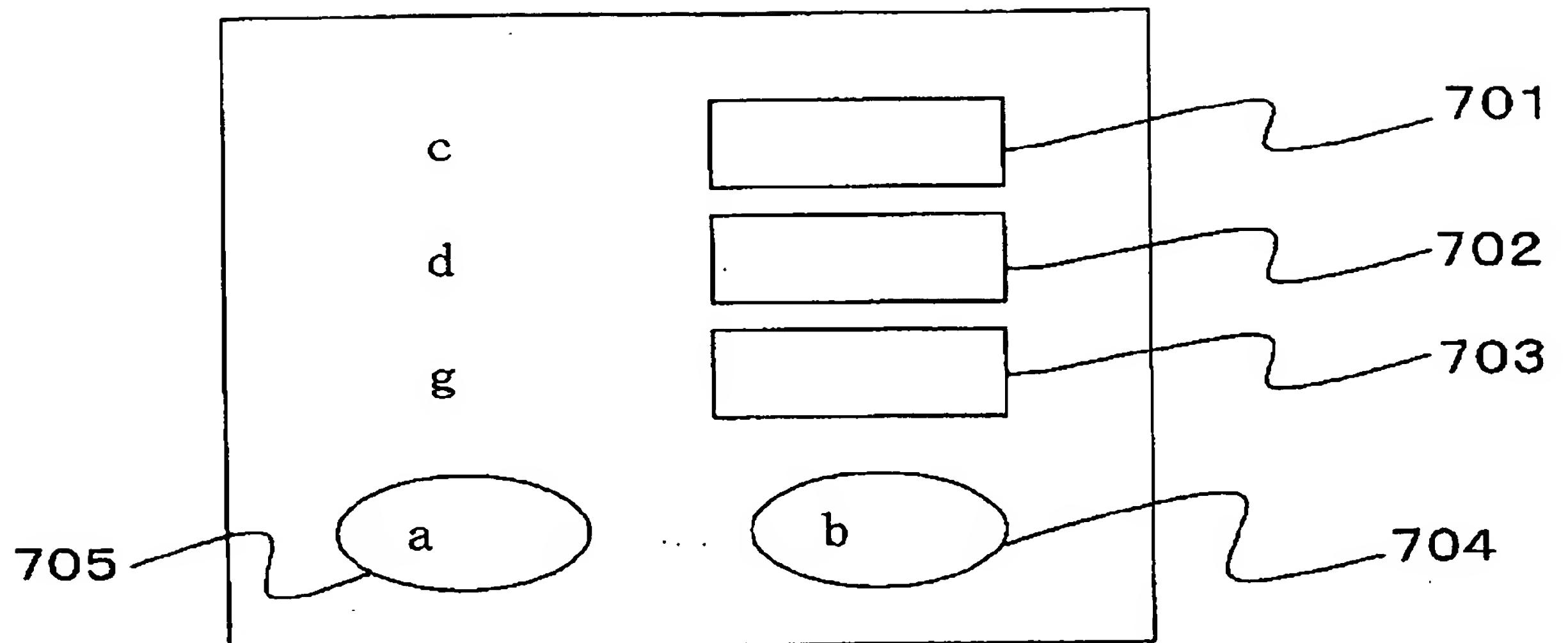
(a) Devided units of Lyee program



(b) Modules composing a basic structure



[Fig. 2]



[Fig.3]

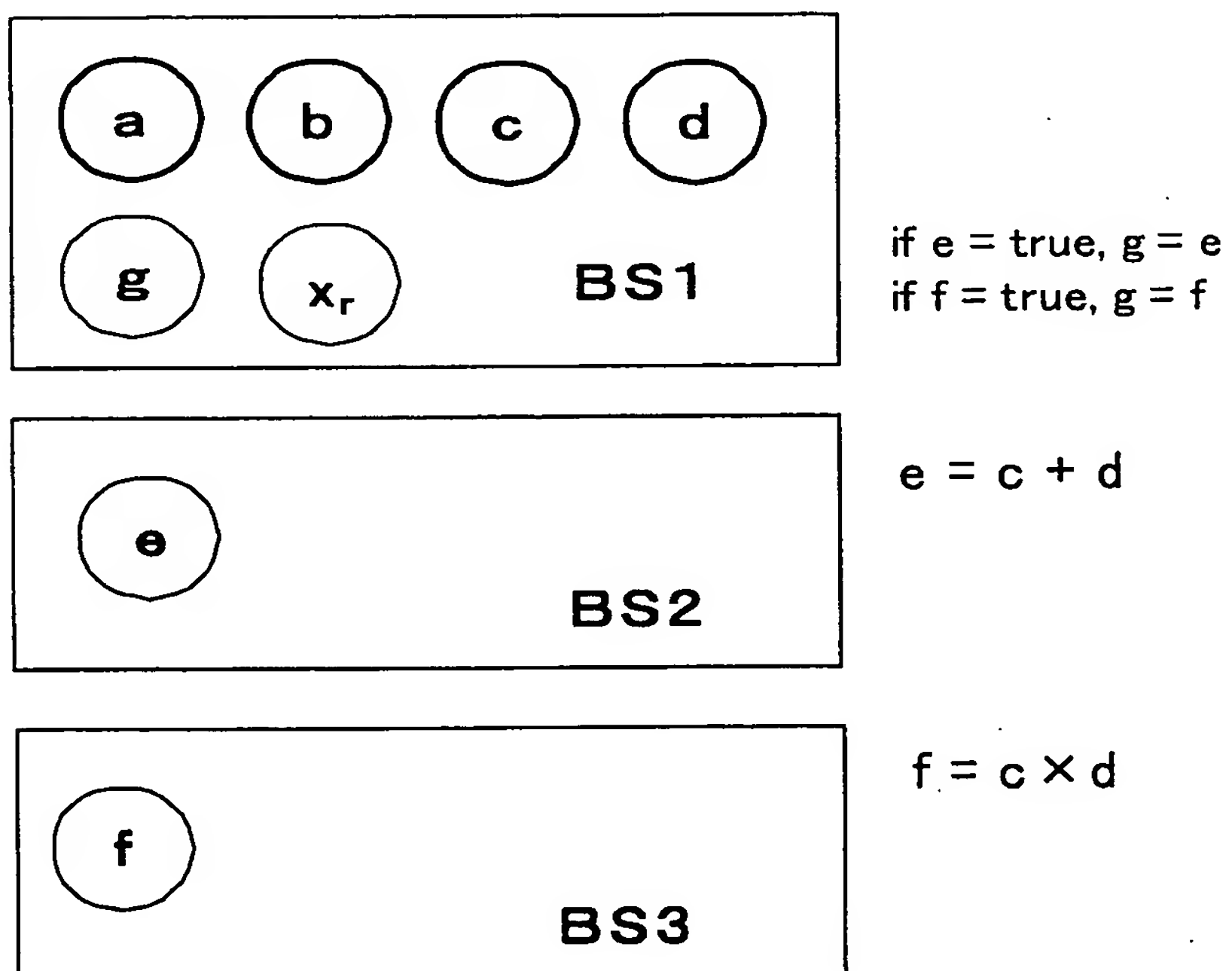


Fig. 4

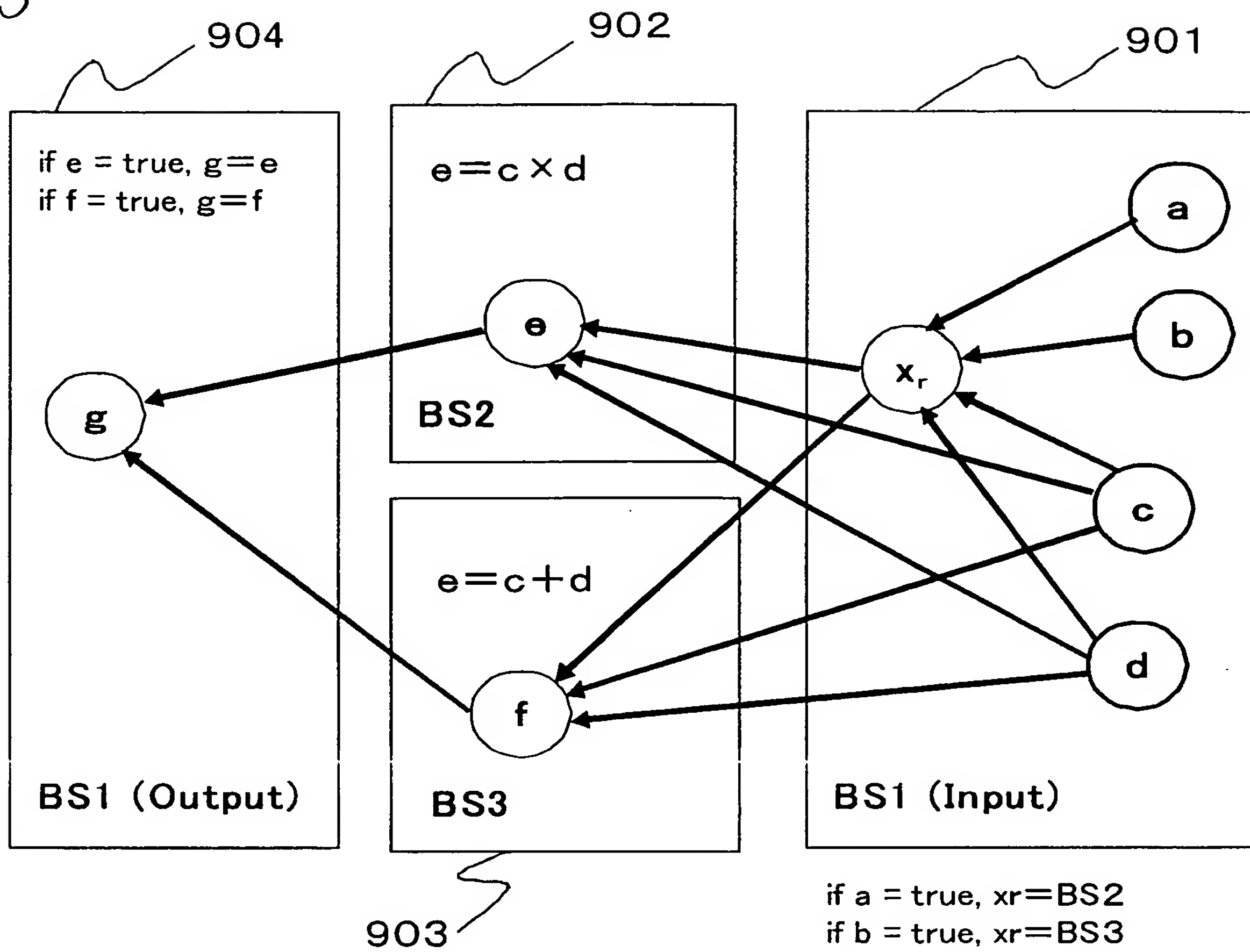


Fig. 5

	<i>a</i>	<i>b</i>	<i>c</i>	<i>d</i>	<i>x_r</i>	<i>e</i>	<i>f</i>	<i>g</i>
<i>a</i>	0	0	0	0	0	0	0	0
<i>b</i>	0	0	0	0	0	0	0	0
<i>c</i>	0	0	0	0	0	0	0	0
<i>d</i>	0	0	0	0	0	0	0	0
<i>x_r</i>	1	1	1	1	0	0	0	0
<i>e</i>	0	0	1	1	1	0	0	0
<i>f</i>	0	0	1	1	1	0	0	0
<i>g</i>	0	0	0	0	0	1	1	0

1001

1006

1007

1002

1008

1003

1005

1004

Fig. 6

	<i>a</i>	<i>b</i>	<i>c</i>	<i>d</i>	<i>x_r</i>	<i>e</i>	<i>f</i>	<i>g</i>
<i>a</i>	0	0	0	0	0	0	0	0
<i>b</i>	0	0	0	0	0	0	0	0
<i>c</i>	0	0	0	0	0	0	0	0
<i>d</i>	0	0	0	0	0	0	0	0
<i>x_r</i>	1	1	1	1	0	0	0	0
<i>e</i>	0	0	1	1	1	0	0	0
<i>f</i>	0	0	1	1	1	0	0	0
<i>g</i>	0	0	0	0	0	1	1	0

1 3 0 1

Fig. 7

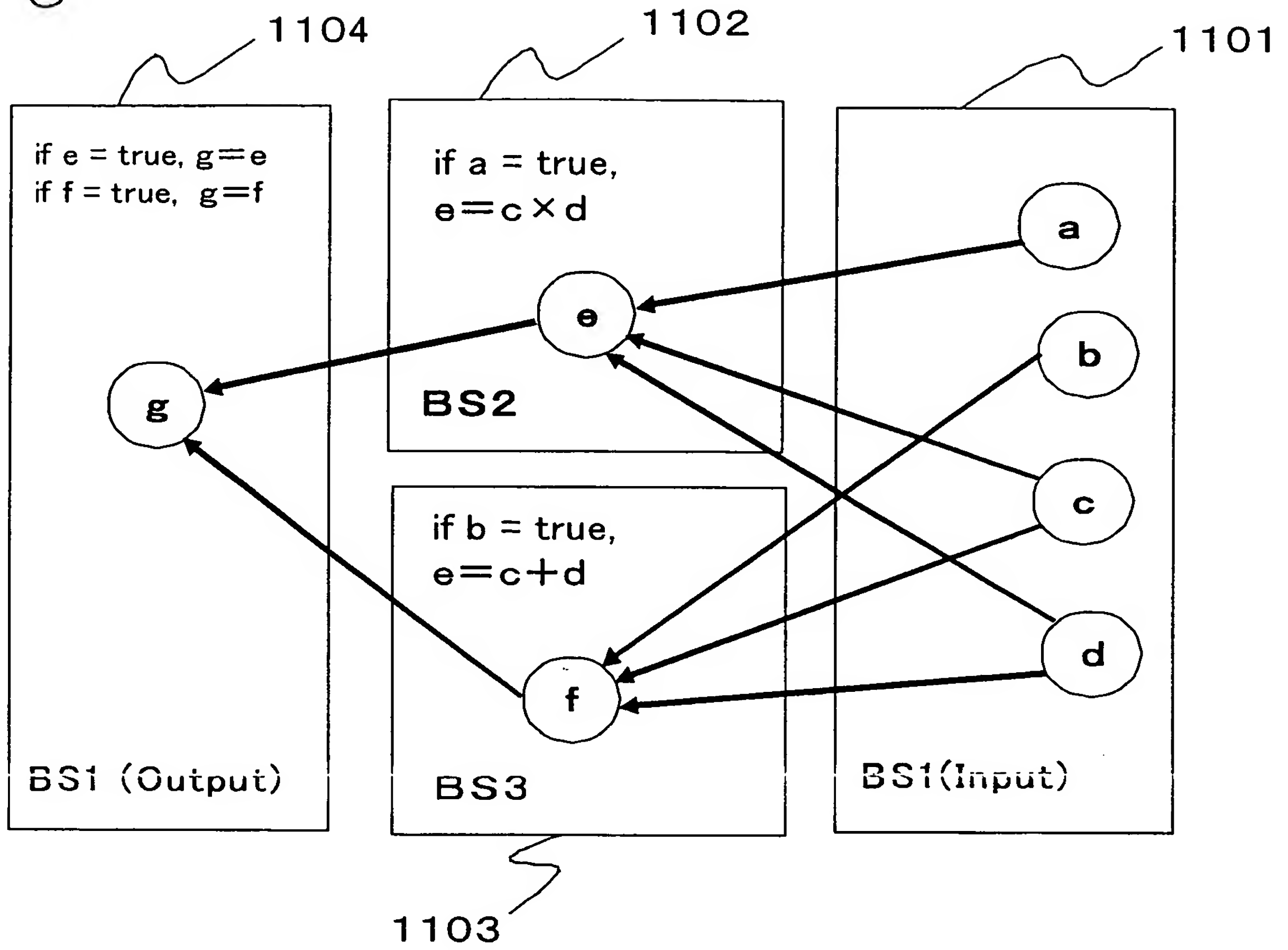


Fig. 8

The diagram shows a 7x7 matrix with columns labeled *a* through *g* and rows labeled *a* through *g*. The matrix contains binary values (0 and 1). Various regions are highlighted with boxes and labels:

- 1201**: Points to the top-left region (rows *a*-*d*, columns *a*-*d*).
- 1206**: Points to the top-right region (rows *a*-*d*, columns *e*-*f*).
- 1207**: Points to the rightmost column (*g*).
- 1202**: Points to the bottom-right region (rows *e*-*g*, columns *e*-*f*).
- 1208**: Points to the bottom-right region (rows *e*-*g*, columns *e*-*f*).
- 1204**: Points to the bottom-left region (rows *e*-*g*, columns *a*-*d*).
- 1205**: Points to the bottom row (*g*).
- 1203**: Points to the bottom row (*g*).

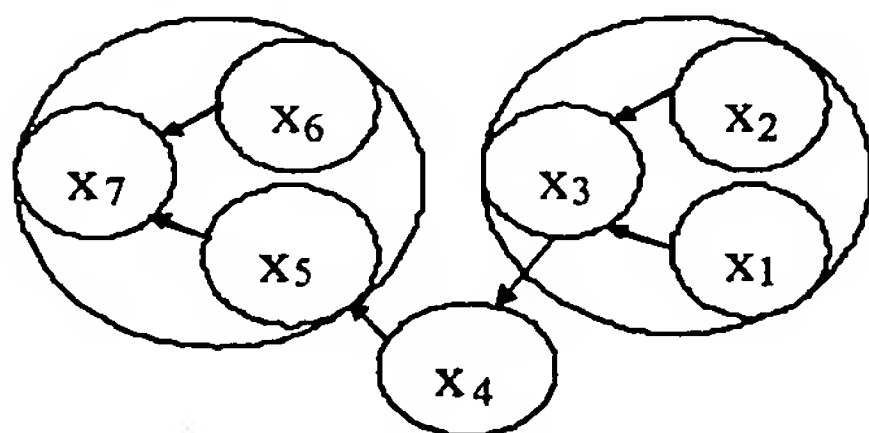
	<i>a</i>	<i>b</i>	<i>c</i>	<i>d</i>	<i>e</i>	<i>f</i>	<i>g</i>
<i>a</i>	0	0	0	0	0	0	0
<i>b</i>	0	0	0	0	0	0	0
<i>c</i>	0	0	0	0	0	0	0
<i>d</i>	0	0	0	0	0	0	0
<i>e</i>	1	0	1	1	0	0	0
<i>f</i>	0	1	1	1	0	0	0
<i>g</i>	0	0	0	0	1	1	0

Fig. 9

	<i>a</i>	<i>b</i>	<i>c</i>	<i>d</i>	<i>e</i>	<i>f</i>	<i>g</i>
<i>a</i>	0	0	0	0	0	0	0
<i>b</i>	0	0	0	0	0	0	0
<i>c</i>	0	0	0	0	0	0	0
<i>d</i>	0	0	0	0	0	0	0
<i>e</i>	1	0	1	1	0	0	0
<i>f</i>	0	1	1	1	0	0	0
<i>g</i>	0	0	0	0	1	1	0

1 6 0 1

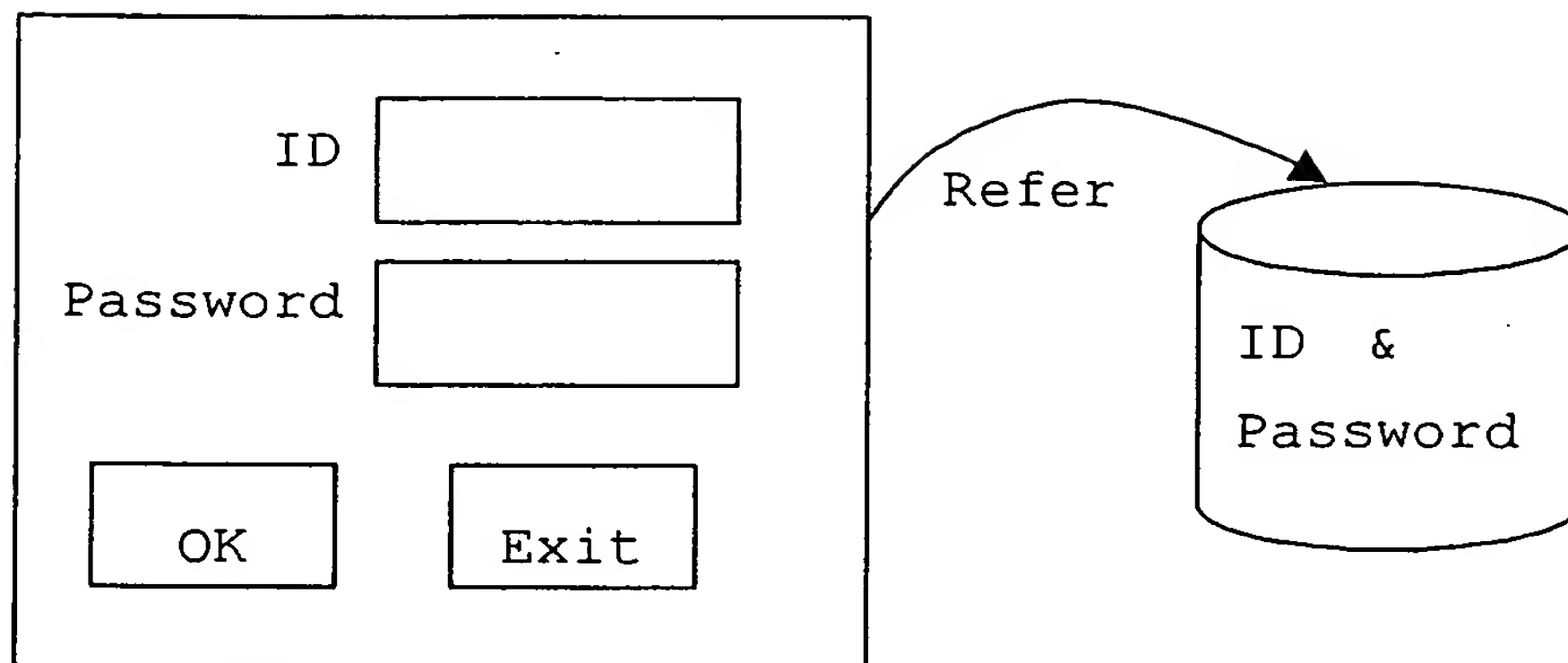
[Fig. 10]



Directed graph of summation

Fig. 11

Initial



[Fig. 12]

Student Registration

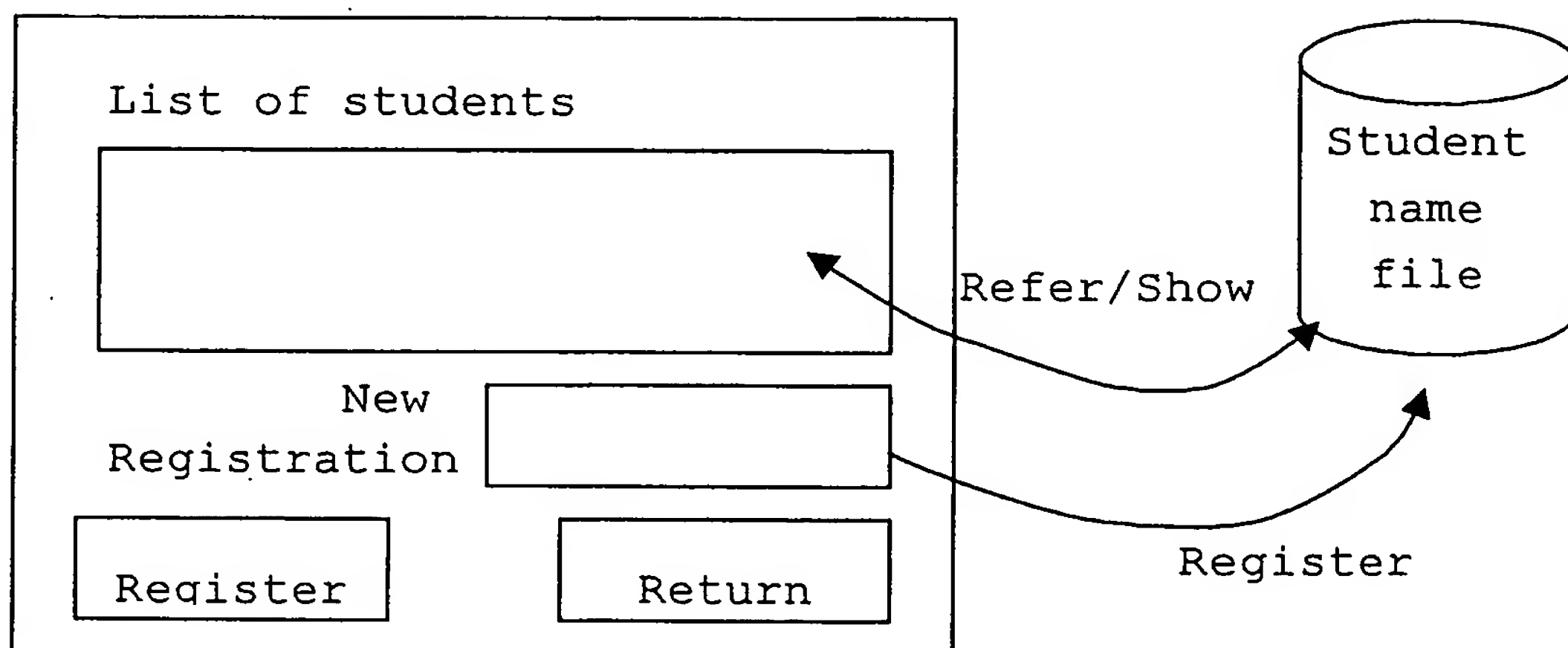


Fig. 13

Result Administration Screen

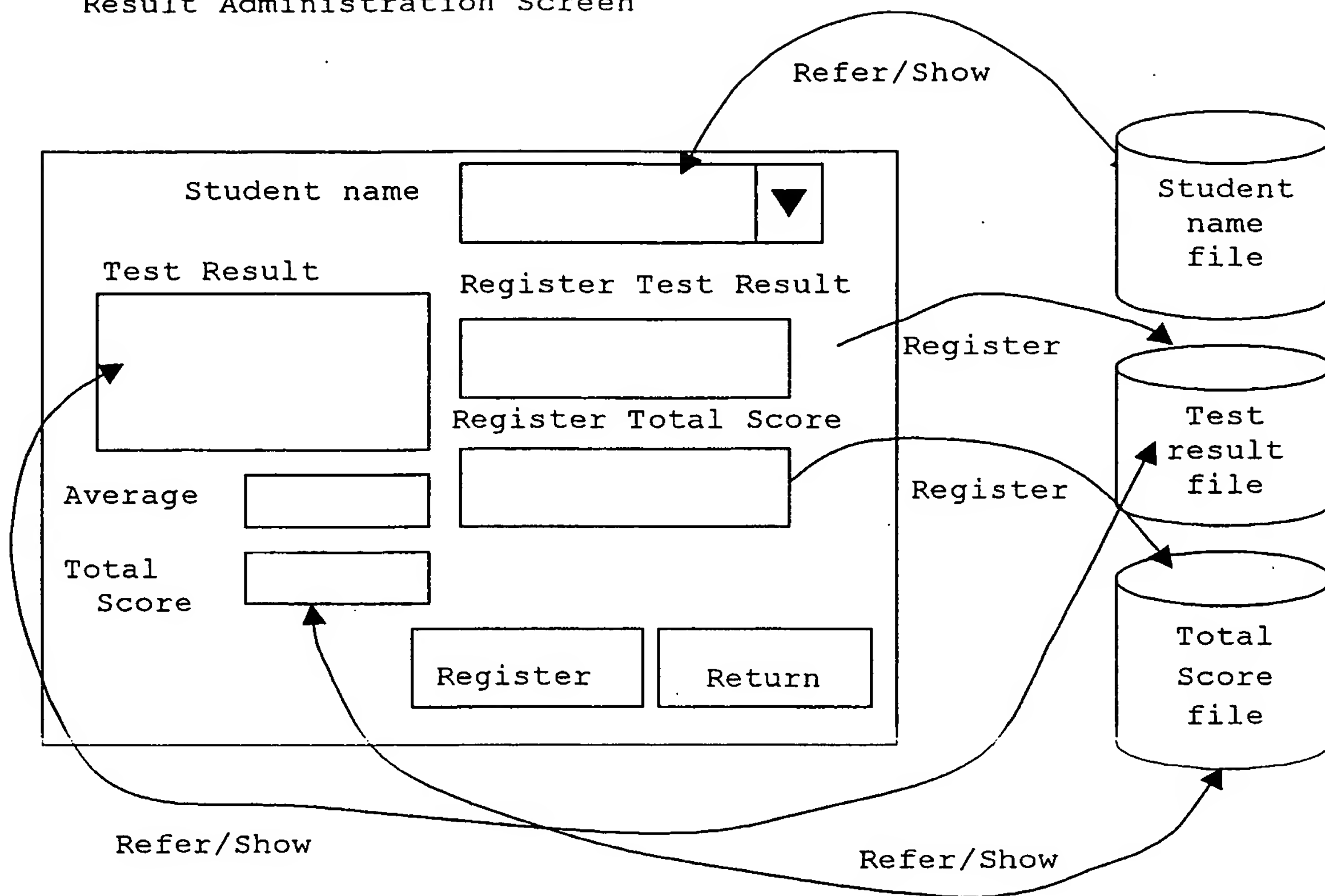


Fig. 14

Process Route Diagram of Result Administration Program

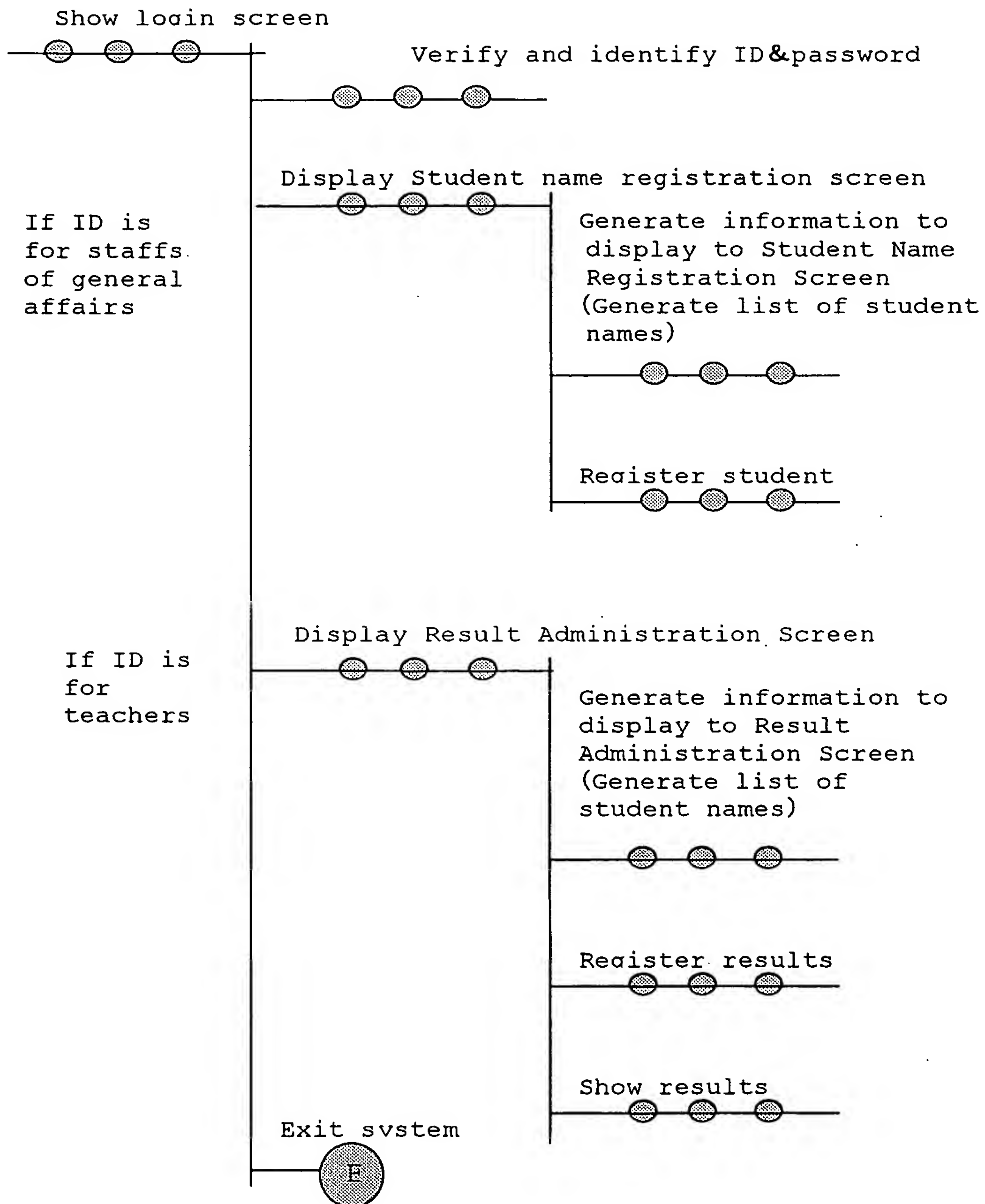


Fig. 15

```
1 //S1
2 //S1W04
3 W04_PS_PBOXRV01_S1I_RV01_S0;          // clear input flag
4 W04_PS_PCR1S1I_S1I_S0;                // clear input area
5 //S1W04 e
6 //FALSE
7 if(W02_S1I.cmdOK == FALSE)              // shared variable from route vector
8 {                                       //
9 //S1W02
10 W02_PI_PRD1RV01_S1I_RV01_S0;          // input from screen
11 strncpy ( W02_S1I.UserID,S1_Buff.UserID, size of (W02_S1I.UserID) );    // input value
12 strncpy ( W02_S1I.Password,S1_Buff.Password, size of (W02_S1I.Password) ); // input value
13 W02_S1I.cmdOK = S1_Buff.cmdOK;         // input function button shared variable
14 W02_S1I.cmdExit = S1_Buff.cmdExit;     // input function button shared variable
15 }                                     // these word are only definition
16 //S1W02 e
17 //FALSE e
18 //Exit
19 //S1W03
20 . if ( W02_S1I.cmdExit == TRUE)        // shared variable from route vector
21 {                                       //
22 // W03_PN_PNTES1W03_S0; /* S1-W03 */ // eliminated route vector
23
```

A part of the program

Fig. 16

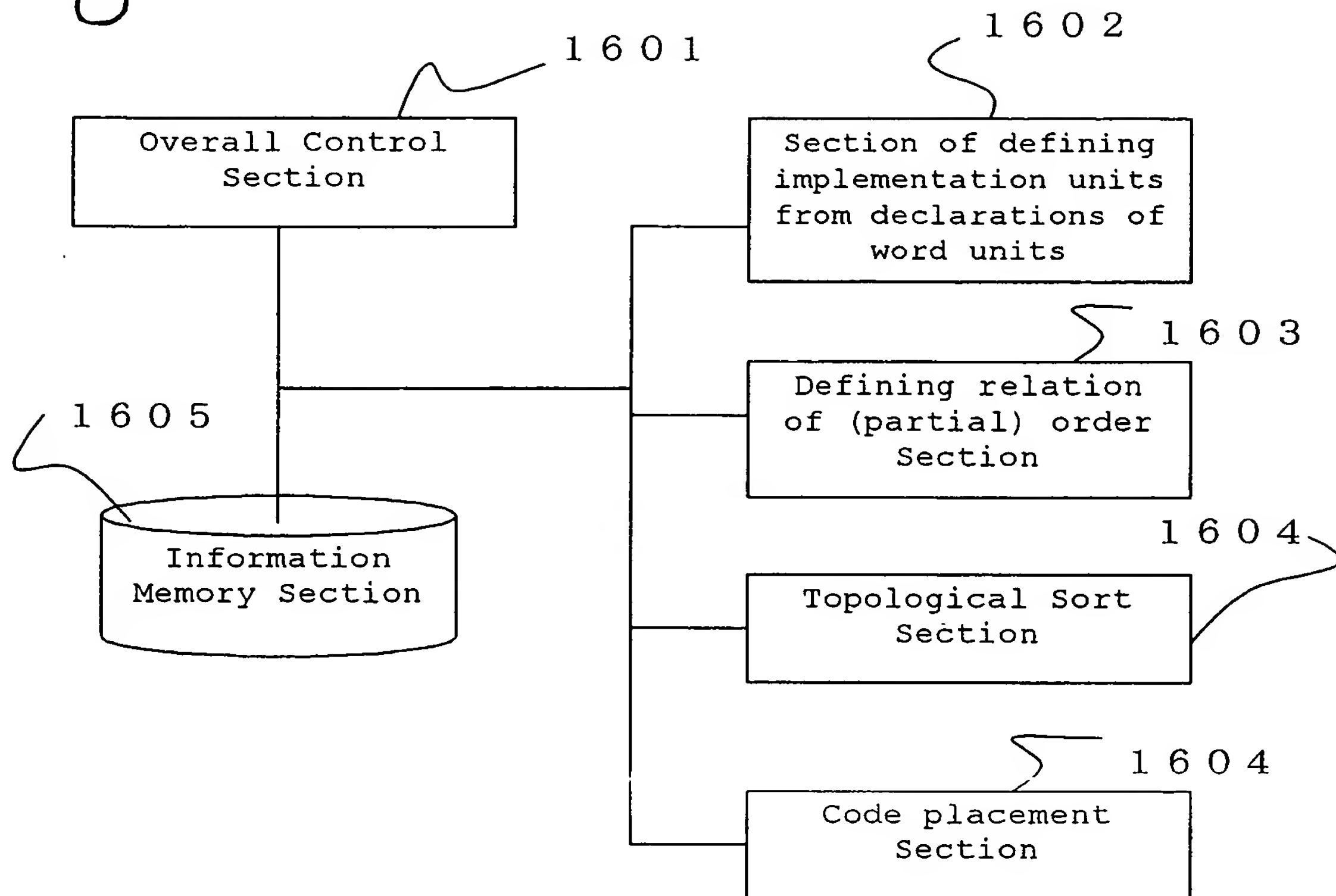


Fig. 17

